

Figure 2(a)

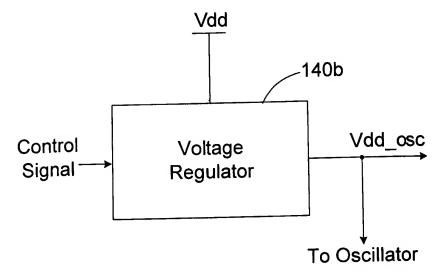


Figure 2(b)

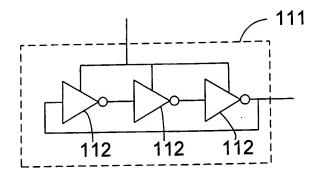
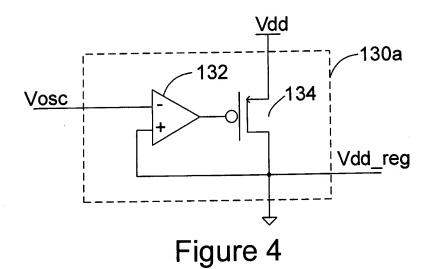
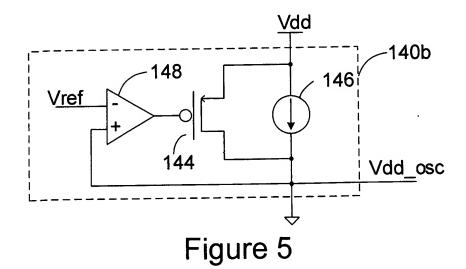


Figure 3





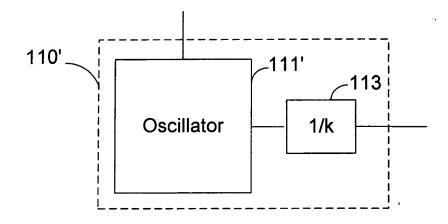


Figure 6

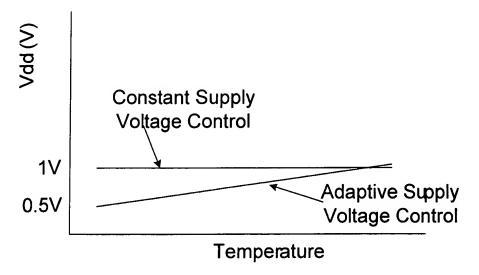
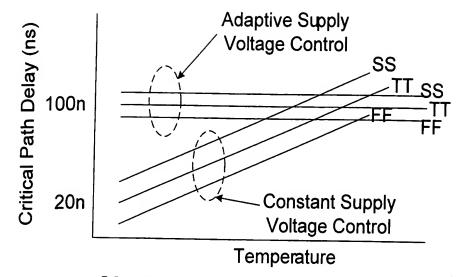
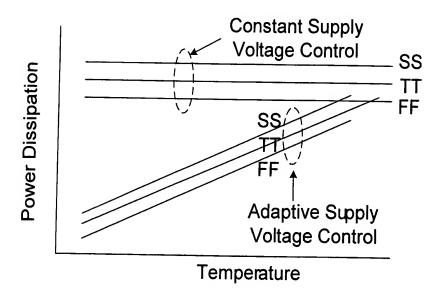


Figure 7(a)



SS: Slow Process
TT: Typical Process
FF: Fast Process

Figure 7(b)



SS: Slow Process
TT: Typical Process
FF: Fast Process

Figure 7(c)

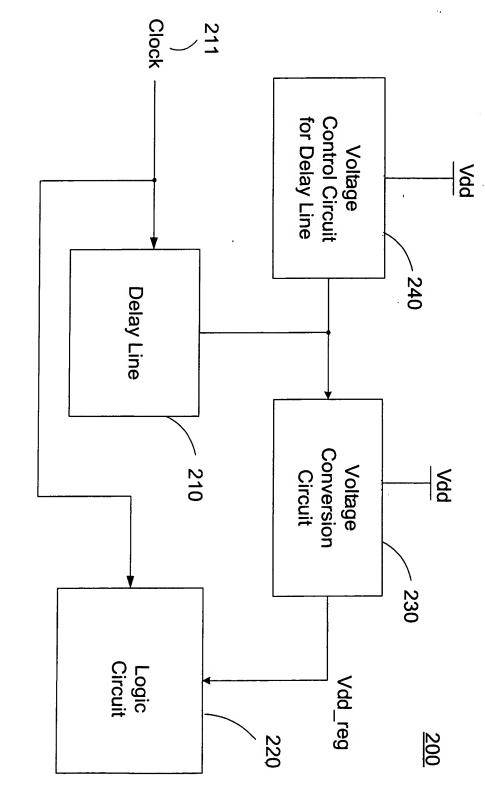


Figure 8

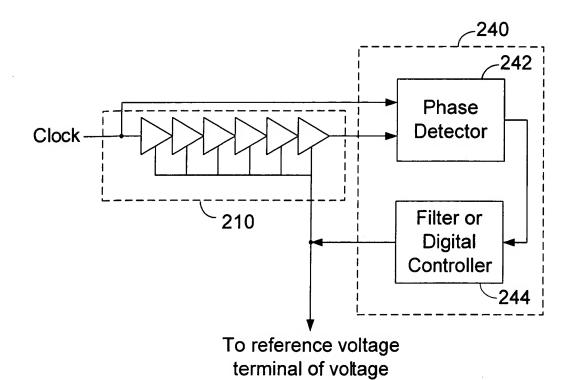


Figure 9

conversion circuit